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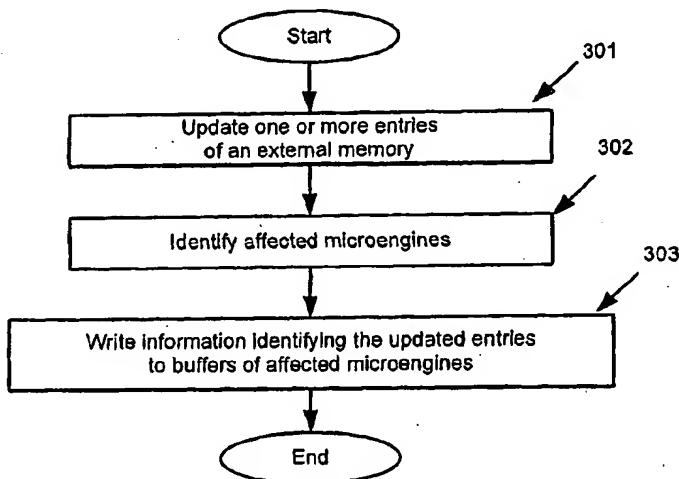
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(54) Title: UPDATING ENTRIES CACHED BY A NETWORK PROCESSOR



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(57) Abstract: Machine-readable media, methods, and apparatus are described to update entries cached in a network processor. In some embodiments, microengines of a network processor cache entries in corresponding local memories and update cached entries based upon information stored in corresponding buffers for the microengines. A control plane of the network processor identifies each microengine having updated entry stored in corresponding local memory, and store information in the corresponding buffer for each identified microengine to indicate that the entry has been updated in the external memory.



*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*